



AI Acceleration

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Welcome to All Developers!

Data scientists

Frameworks: Python, APIs

DEEPhi
深鉴科技

Caffe

mxnet

FFMPEG

TensorFlow

SaaS developers

FaaS Platform

aws

HUAWEI

Aliyun
Alibaba Cloud Computing

NIMBIX

Application developers

SDX: C++, OpenCL, Libraries

Linux

RTOS

Xen

Embedded developers

Embedded Software: MPSoC

Hardware-aware
Software developers

HLS: C++ IP Functions

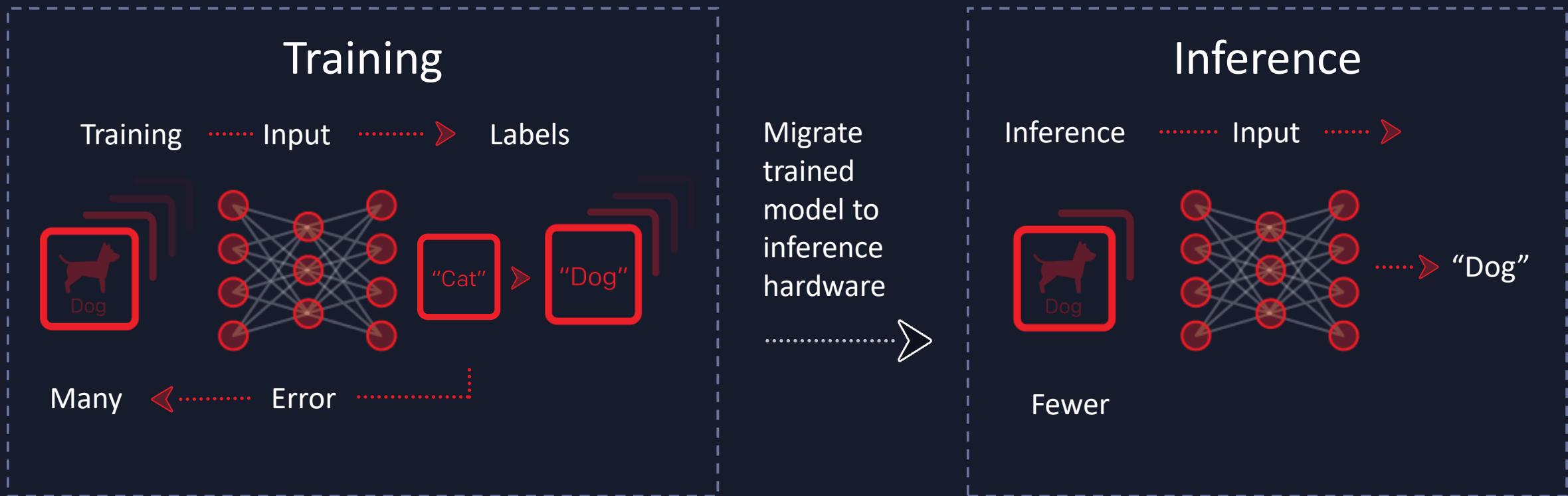
System integrators

IP Integrator: System Integration

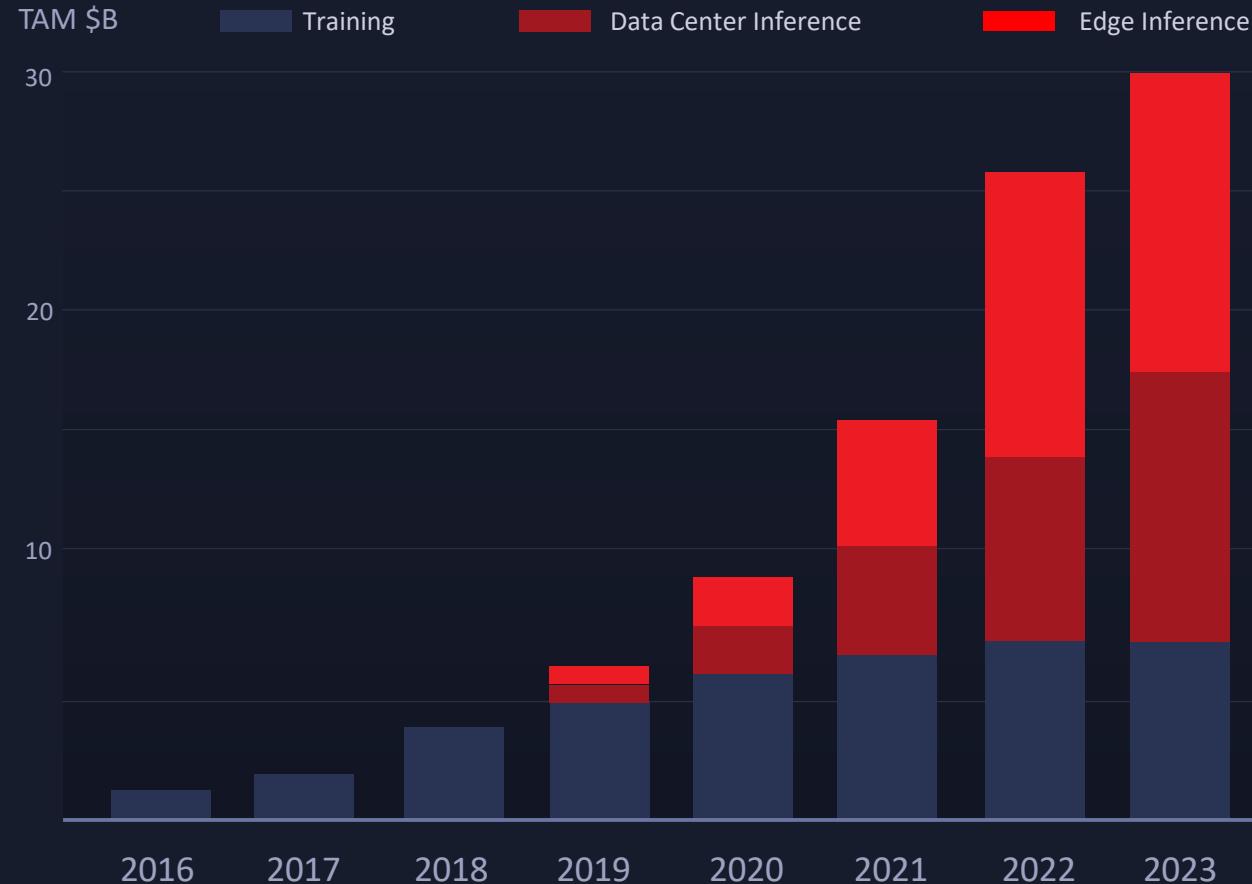
Hardware developers

Vivado Design Suite: RTL Full Design

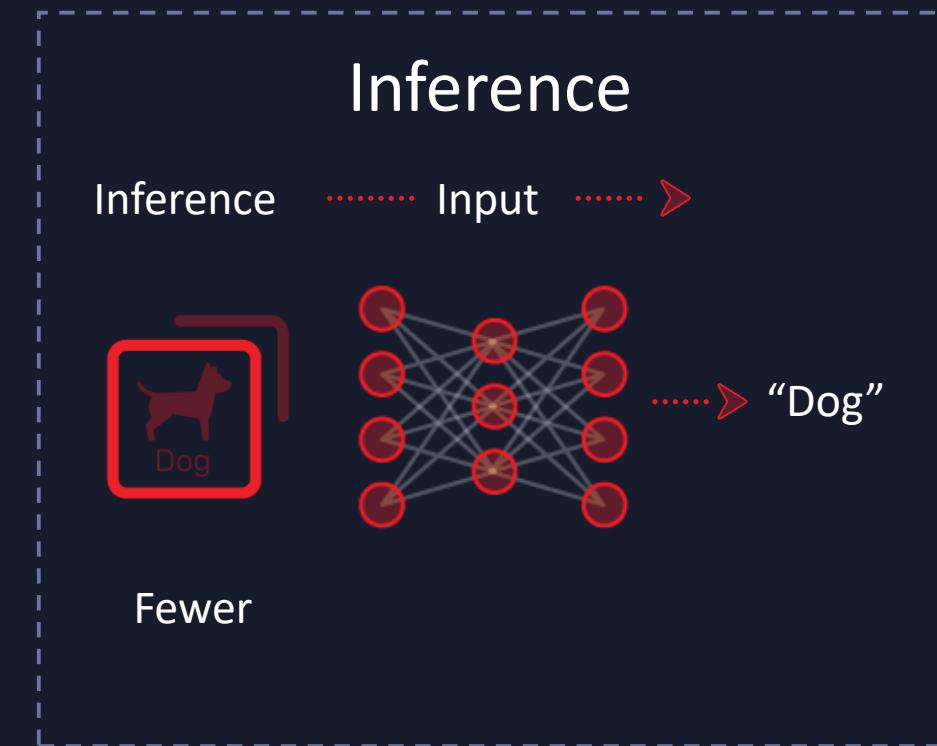
➤ Training vs. Inference



Inference Projected Growth



Barclays Research, Company Reports May 2018



Inference Challenges



The rate of AI innovation



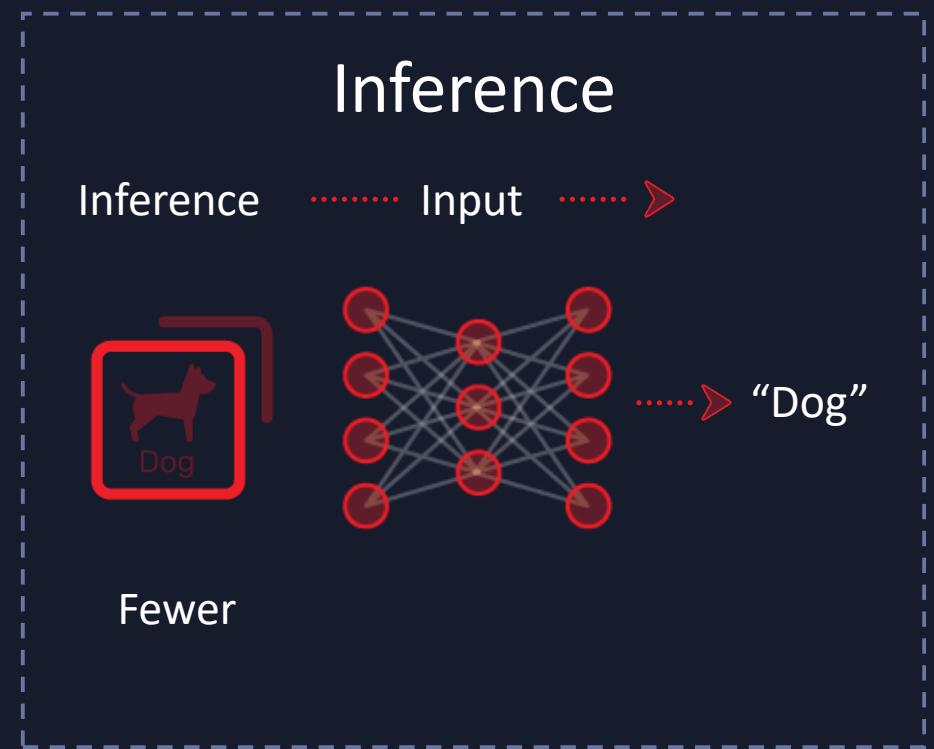
Performance at low latency



Low power consumption



Whole app acceleration



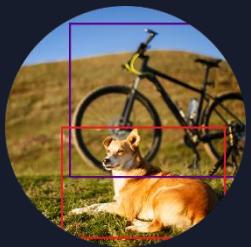
➤ The Rate of AI Model Innovation

APPLICATIONS

Classification



Object Detection



Segmentation



Speech Recognition



Recommendation Engine



Anomaly Detection



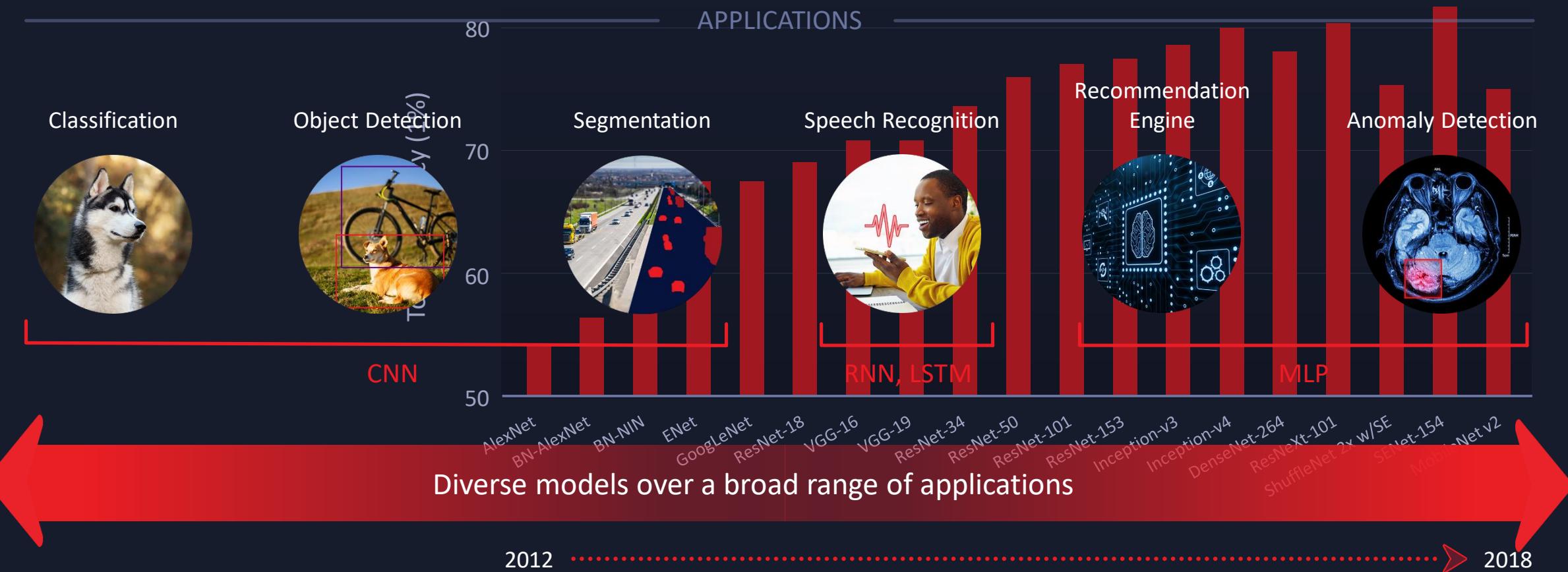
CNN

RNN, LSTM

MLP

Diverse models over a broad range of applications

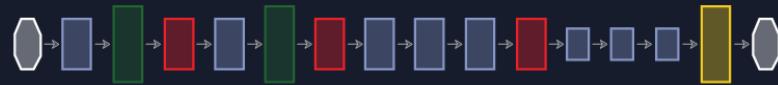
➤ The Rate of AI Model Innovation: Classification



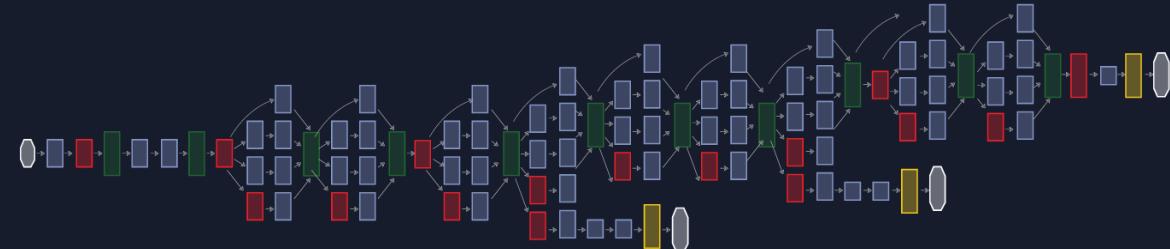


➤ Network Complexity is Growing

AlexNet



GoogLeNet



DenseNet





Inference is Moving to Lower Precision

RELATIVE ENERGY COST

Operation:	Energy (pJ)
8b Add	0.03
16b Add	0.05
32b Add	0.1
16b FP Add	0.4
32b FP Add	0.9



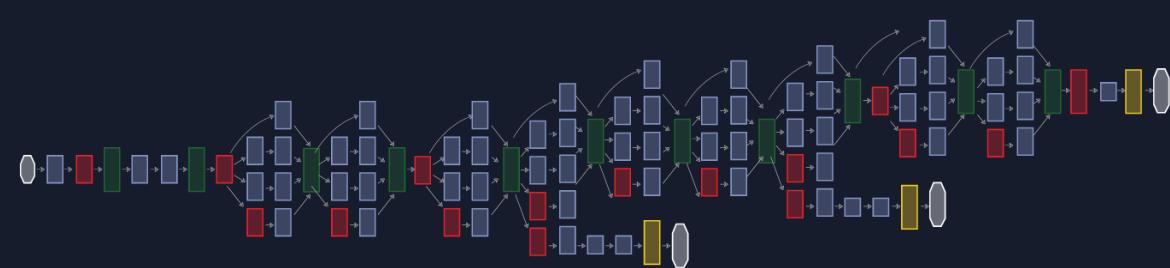


➤ Rate of Innovation Outpaces Silicon Cycles

AlexNet



GoogLeNet



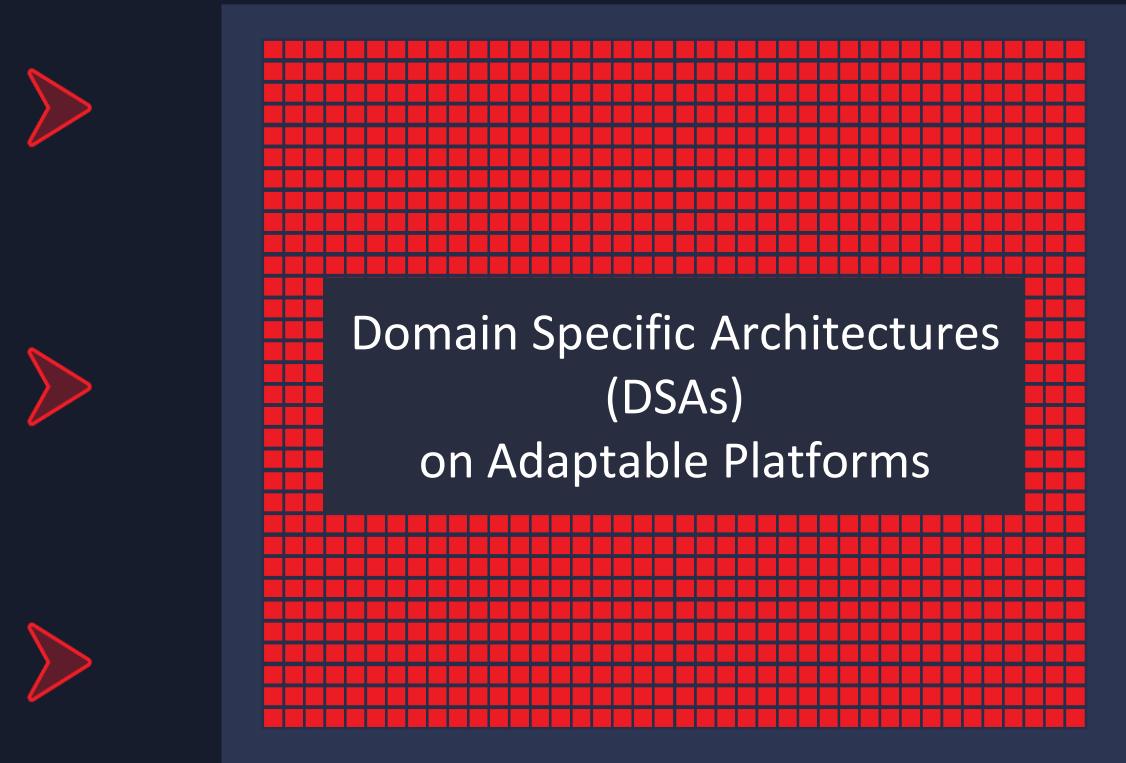
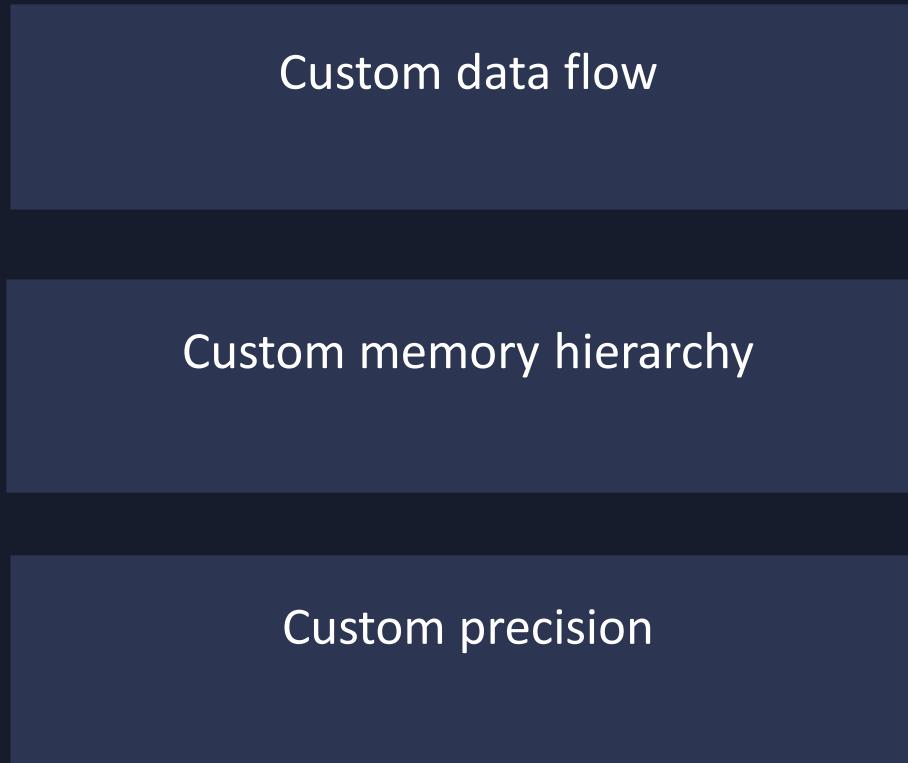
DenseNet



Silicon lifecycle



Only **Adaptable** Hardware Addresses Inference Challenges





➤ Xilinx Acquires DeePhi

Custom data flow



Custom memory hierarchy



Custom precision



DEEPhi 深鉴科技 Now Part of **XILINX**®



Pruning



Quantization

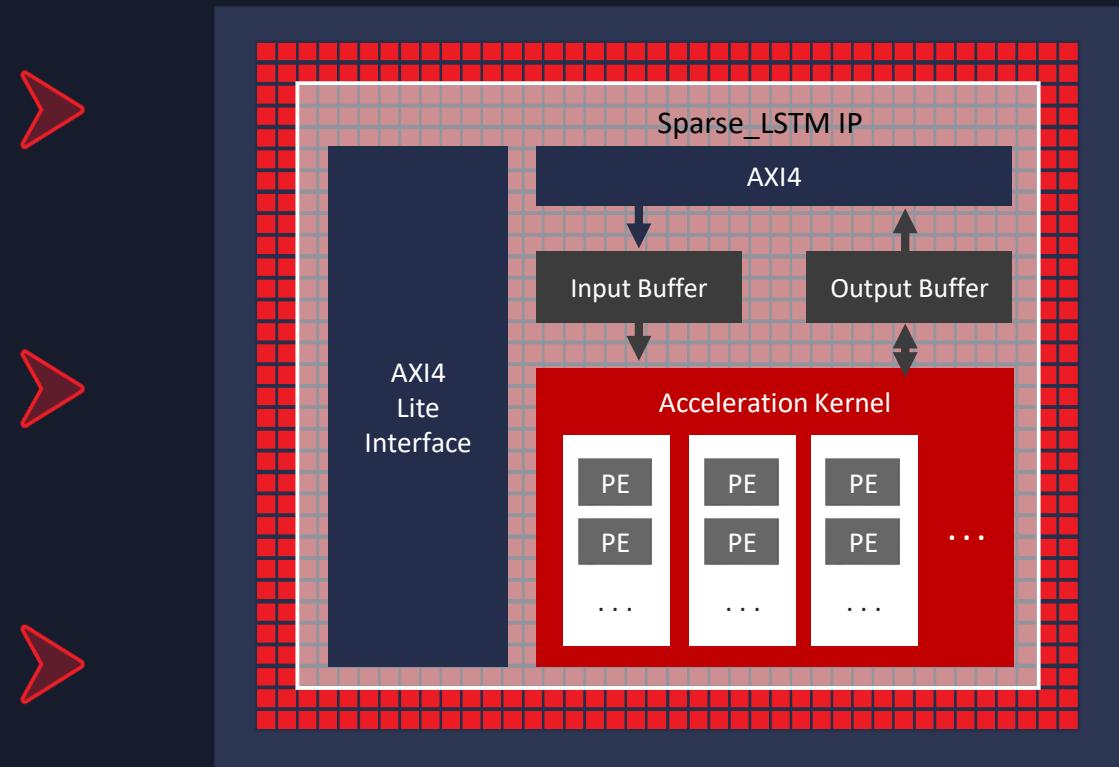
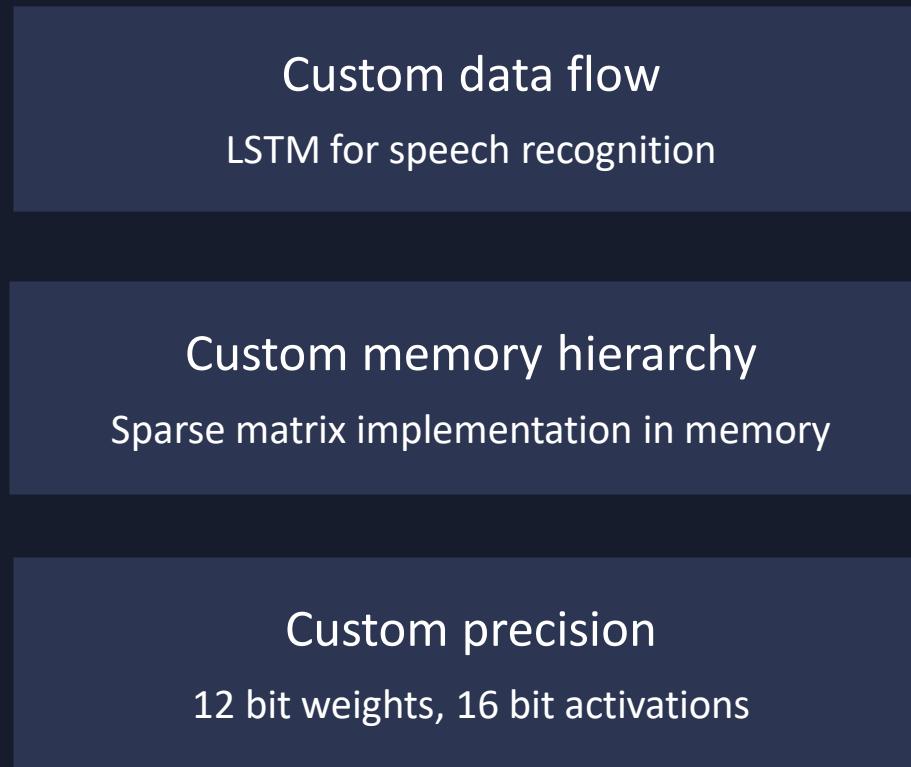


Patented Compression Technology

- Reduces DL accelerator footprint
- Increases performance per watt

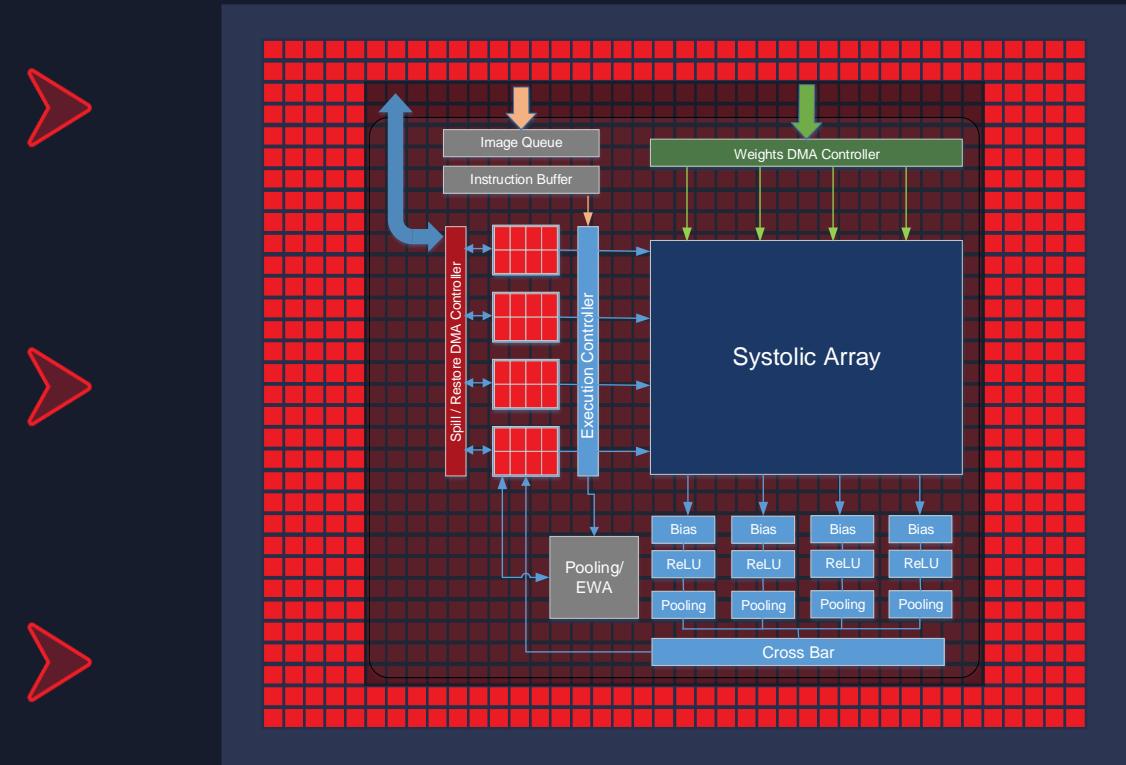
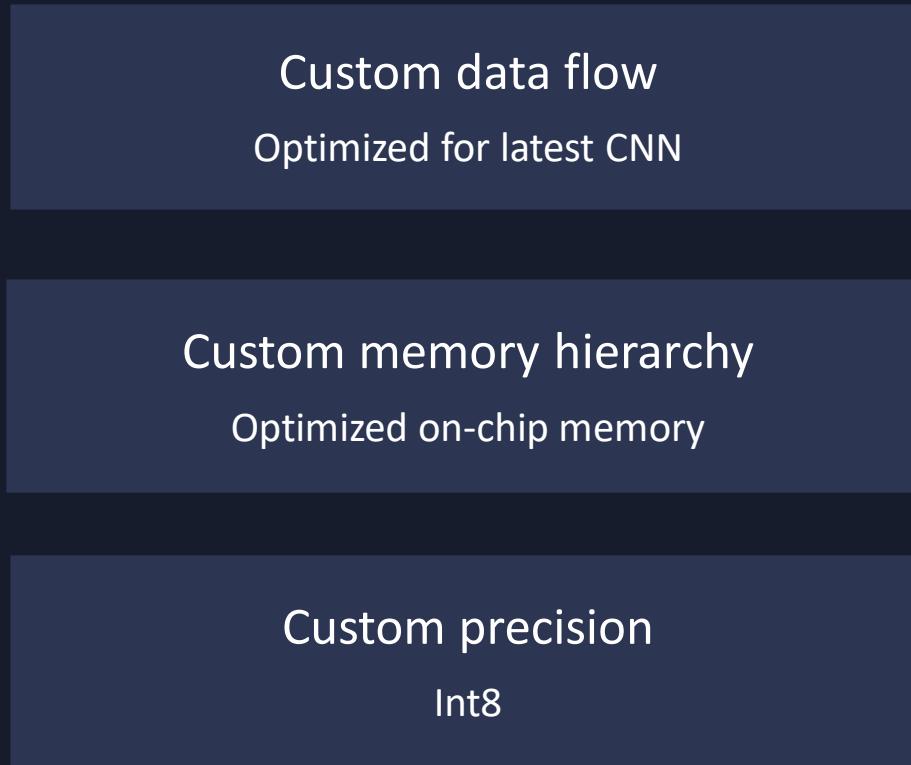


Example: DeePhi LSTM



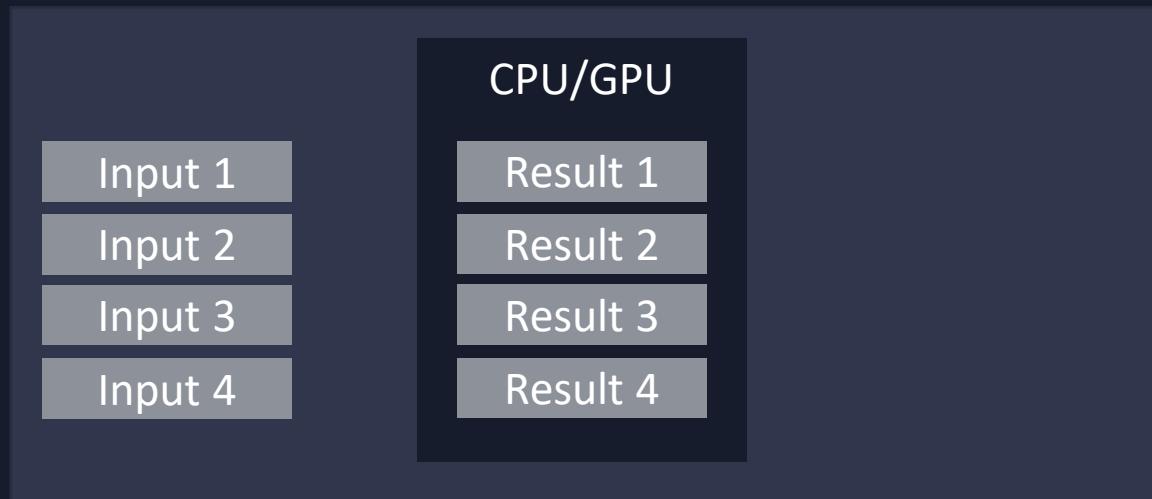


Example: xDNN

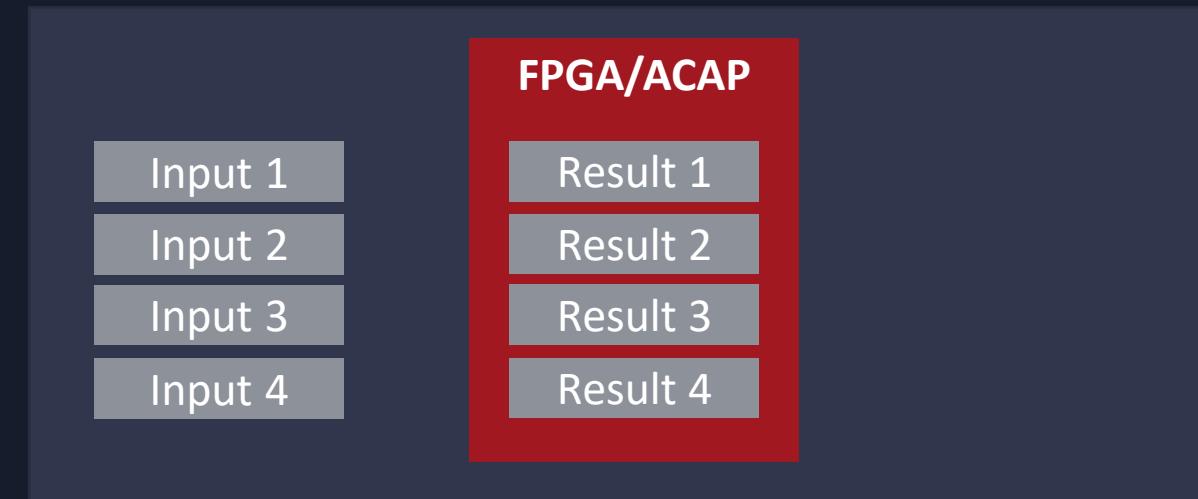




Low Latency is Critical for Inference



High throughput **OR** low latency

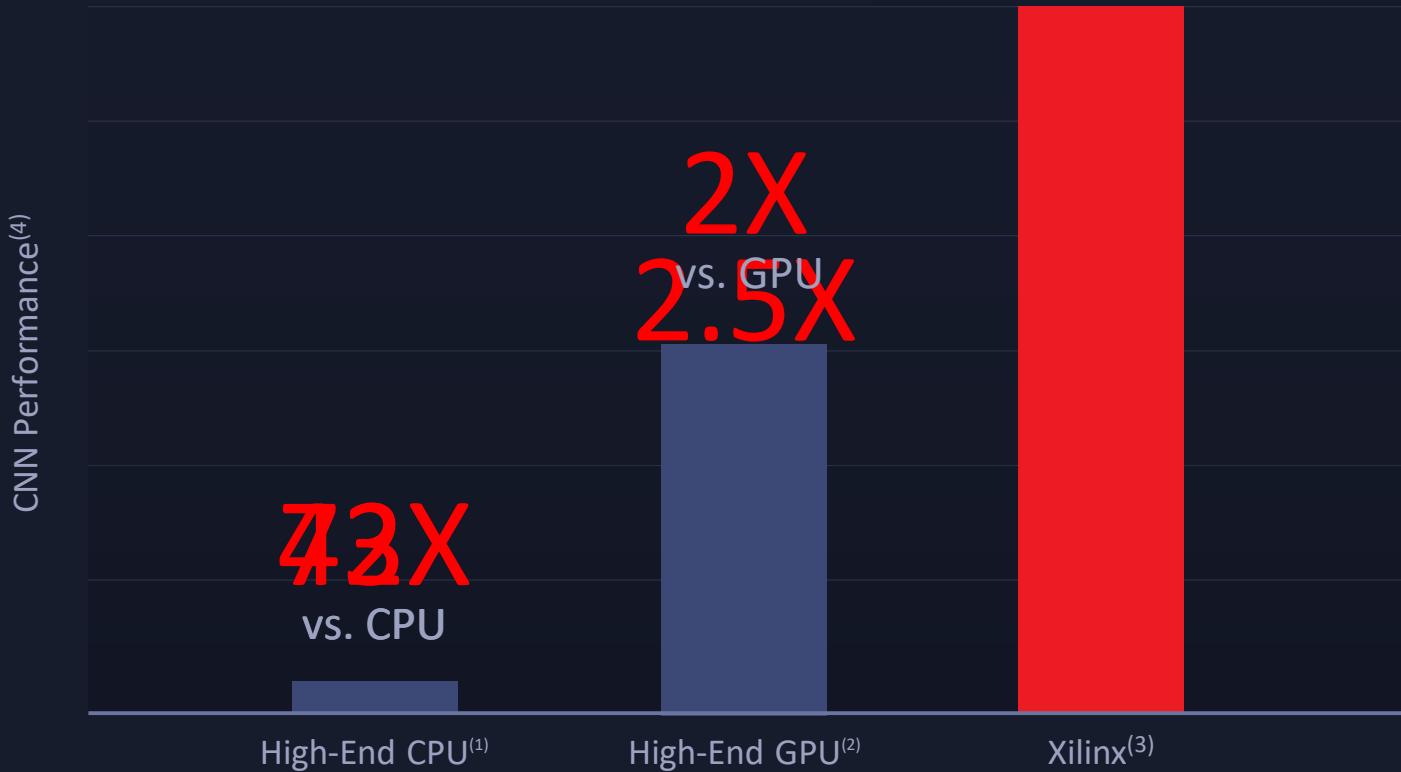


High throughput **AND** low latency



Low Latency: Xilinx's Unique Advantage

Latency Insensitive Inference

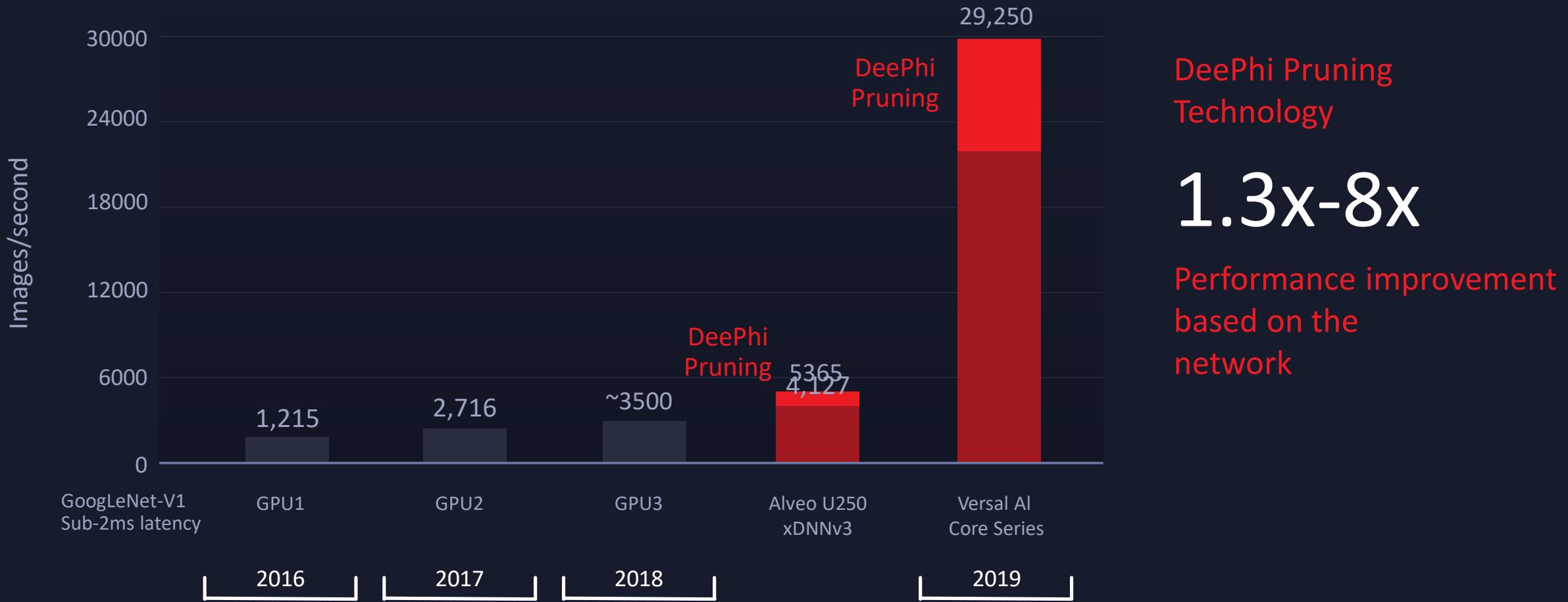


AI Inference Acceleration
Leveraging AI Engines
Majority of Adaptable & Scalar Engines available for Whole App Acceleration

- (1) Measured on EC2 Xeon Platinum 8124 Skylake, c5.18xlarge AWS instance, Intel Caffe: <https://github.com/intel/caffe>
(2) V100 numbers taken from Nvidia Technical Overview, "Deep Learning Platform, Giant Leaps in Performance and Efficiency for AI Services"
(3) Versal Core Series
(4) GoogLeNet V1 throughput (Img/sec)

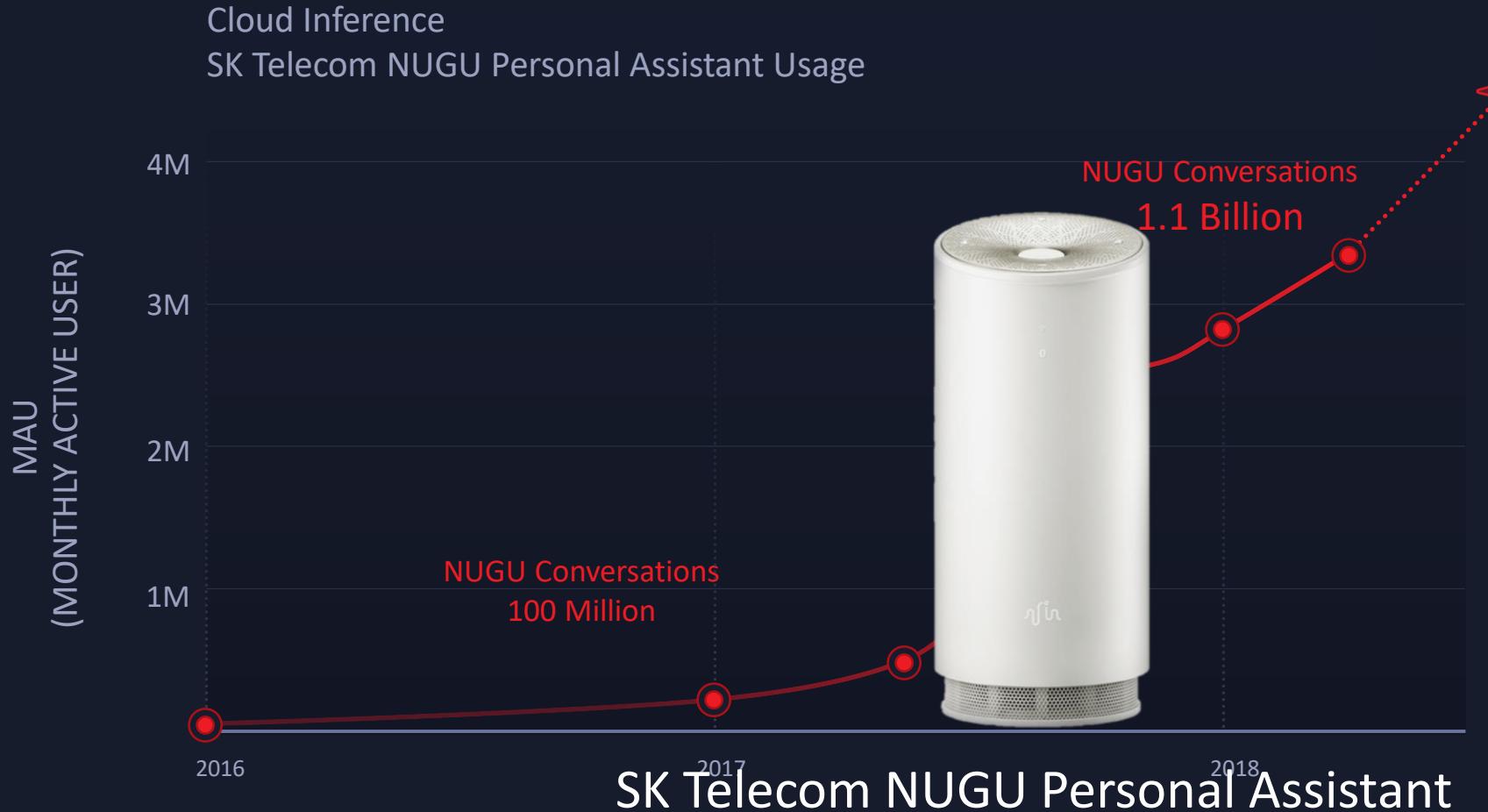


Low-Latency CNN Inference Performance



Sources: Alveo - Published (INT8); Versal - Projected (INT8), 65% PL reserved for whole application; GPU 1 - P4 Published (INT8); GPU 2 - V100 Published (FP16/FP32); GPU 3 - T4 Projected

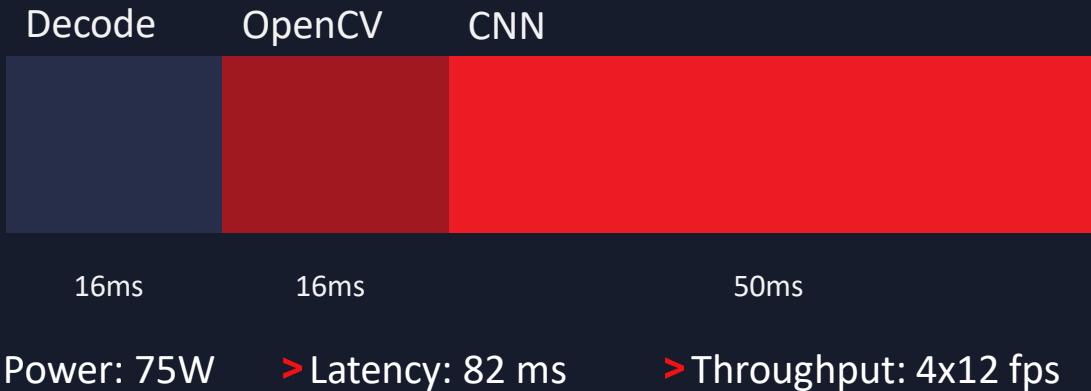
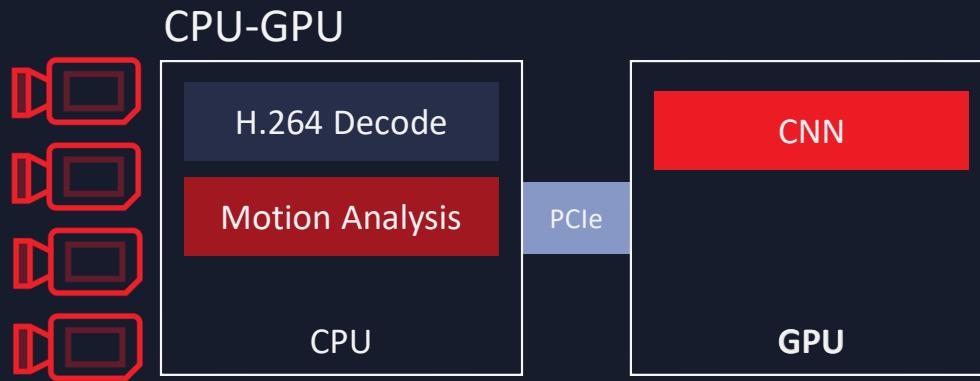
➤ Power Is Critical for Inference Applications



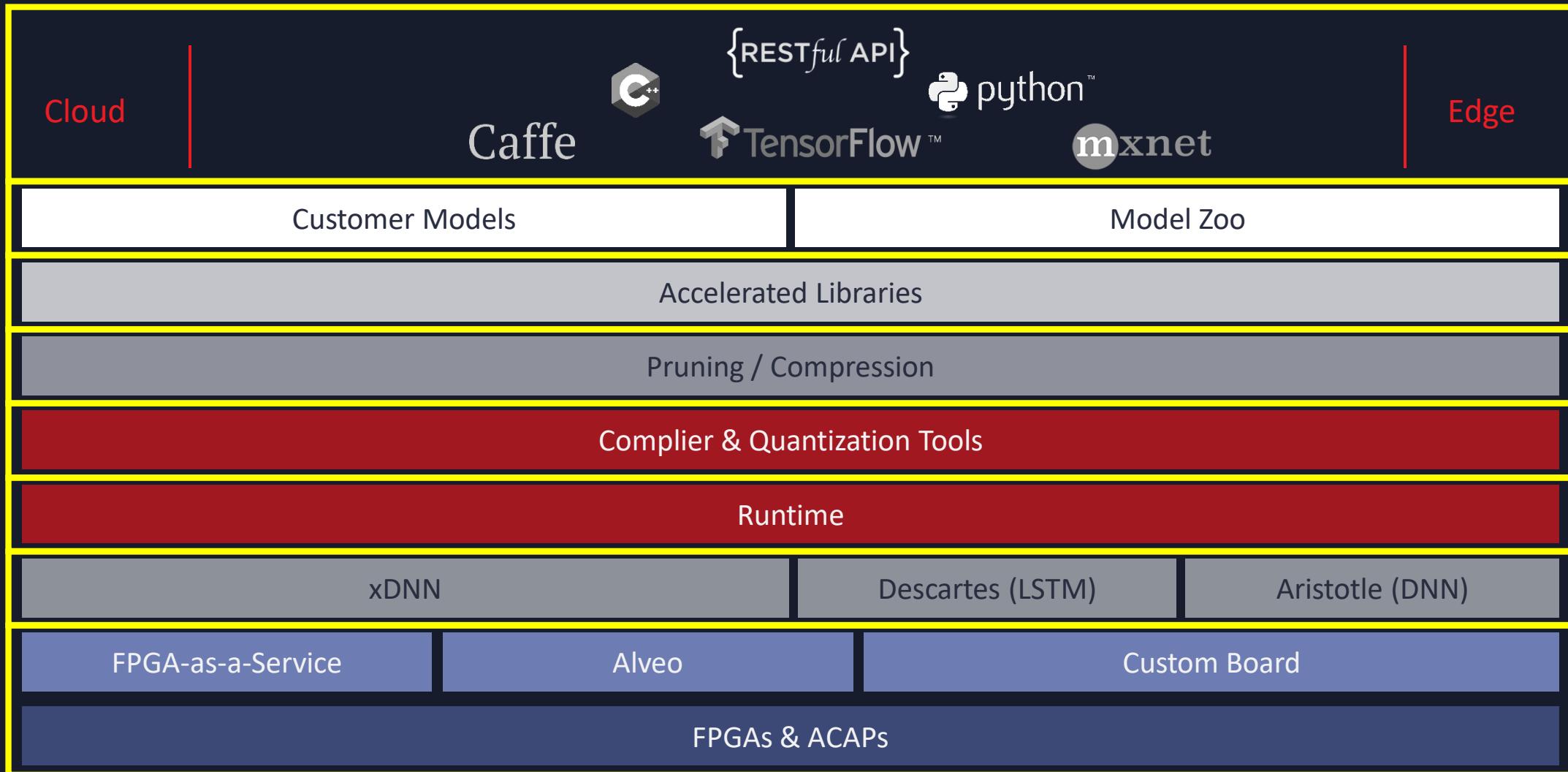
16x
Perf/watt
vs. GPU



➤ Whole Application Acceleration: Smart City / Security



Enabling the Development Community



IN SUMMARY

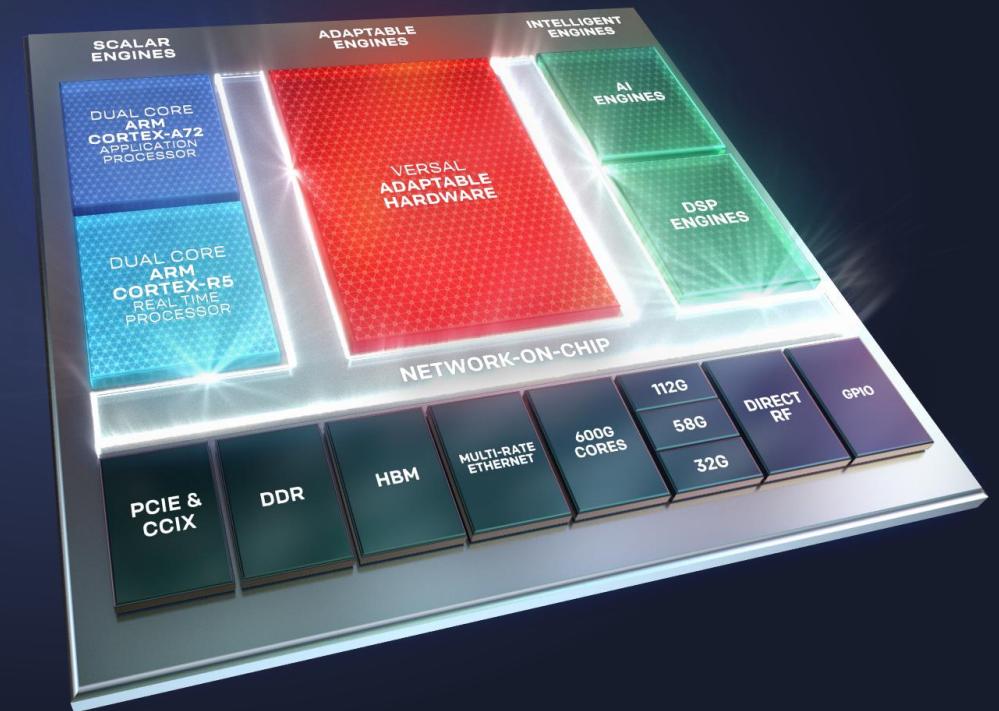
Only Xilinx Adaptable Devices Can:

Match the speed of AI innovation

Give the best performance
at low latency

Give the best power results

Accelerate the whole application



Xilinx

➤ Building
the Adaptable,
Intelligent World

