AMD Vivado

Intelligent Design Software for Adaptive SoC and FPGA Developers

SHORTEN COMPILE TIME. BOOST PERFORMANCE.

Design complexity is growing across industries. Rely on the AMD Vivado™ Design Suite to move fast and optimize adaptive SoCs and FPGAs for today's requirements.

THE FAST PATH TO FMAX

AMD Vivado

Create, integrate, and implement complex designs with the industry's only machine learning–powered electronic design automation tool.
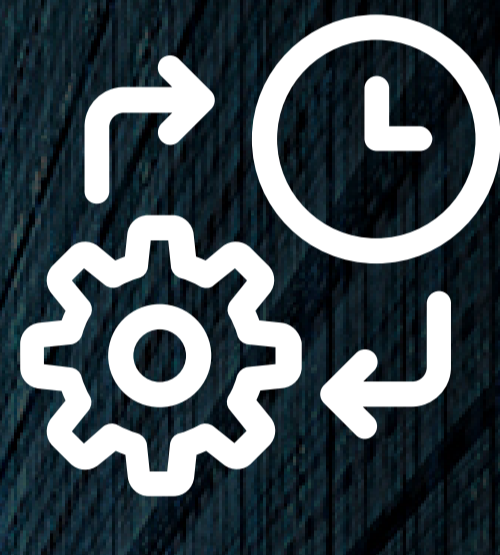
Reach new levels of efficiency

5x
average compile time improvement[1]

Confidently meet performance goals

13%
average improvement in QoR[2]

STREAMLINE THE DESIGN CYCLE

Abstract Shell
**Enable fast compile time and secure collaboration**
- Reduce compile time and memory overhead.
- Drive productivity with team-based design.
- Provide proprietary features for data security in a multi-user environment.

Intelligent Design Runs
Close timing in fewer iterations
- Reduce design iterations with intelligence-based recommendations.
- Efficiently achieve better QoR using machine learning algorithms.
- Score designs and analyze performance automatically.
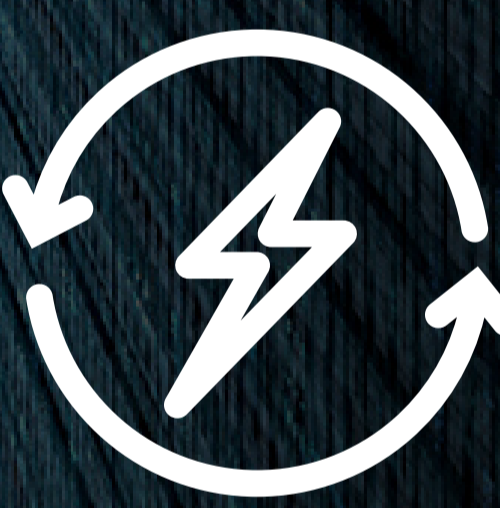
Power Design Manager
Get accurate power estimation from the start
- Confidently evaluate design power consumption.
- Save time and effort early in the design cycle.
- Estimate power accurately for target devices.

MADE FOR GAME-CHANGING AMD ADAPTIVE DEVICES

Adaptive SoCs

AMD ZYNQ 7000   AMD ZYNQ MPSoC   AMD ZYNQ RFSoC   AMD VERSAL

FPGAs

AMD SPARTAN   AMD ARTIX   AMD KINTEX   AMD VIRTEX

VIVADO™ DESIGN SUITE

Achieve top-quality results for complex designs even faster.

[Learn More]   [Download Now]   [Get Power Design Manager]

AMD
together we advance_