



Data Center Acceleration for Software & AI Developers

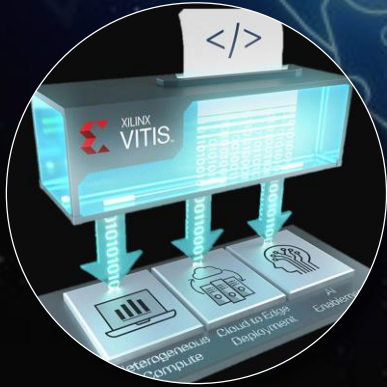
Nick Ni

Director of Product Marketing

AI, Software and Ecosystem



Agenda



Vitis – Unified Software Platform

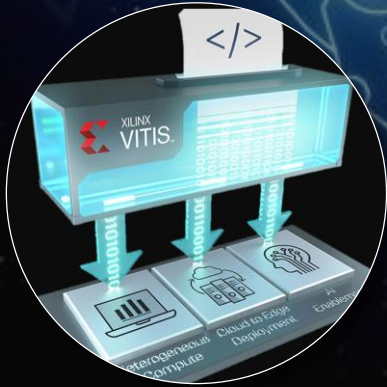


Developer Programs



App Store

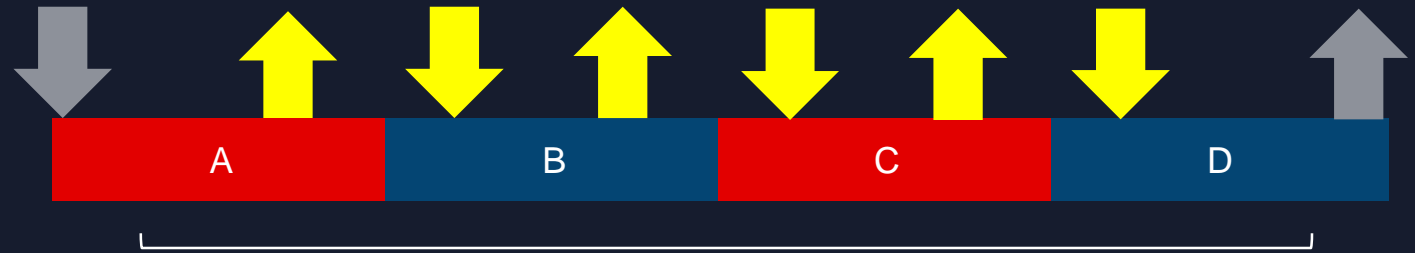
Agenda



Vitis – Unified Software Platform

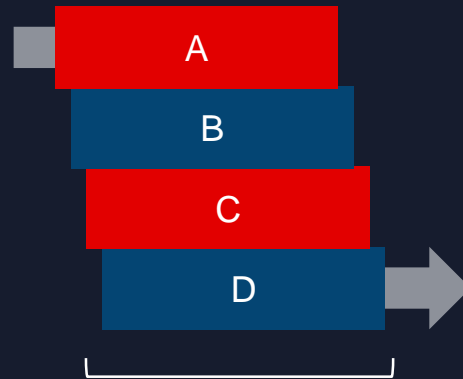
Why Adaptive Computing?

CPU & GPU: Memory Map



$$\text{Latency} = A+B+C+D + 6 * \text{memory latency}$$

FPGA: Streaming



$$\text{Latency} \approx D$$

- ▶ Whole App Acceleration
- ▶ Latency: $A \ll A+B+C+D + 6 * \text{mem}$

Accelerating SW & AI Development w/ HW Abstraction



Opensource

Software Application



AI Acceleration



SW Acceleration

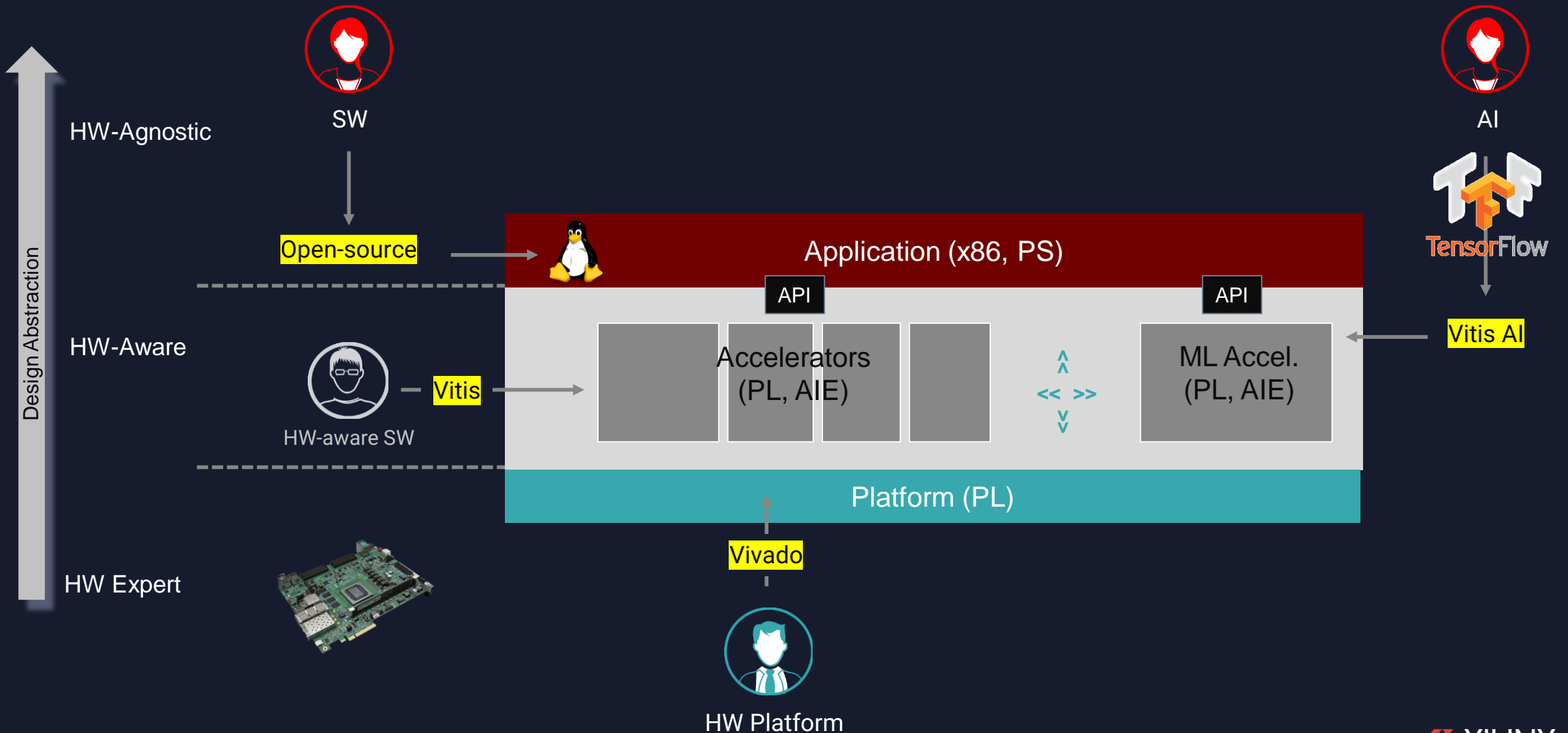
C++, Python



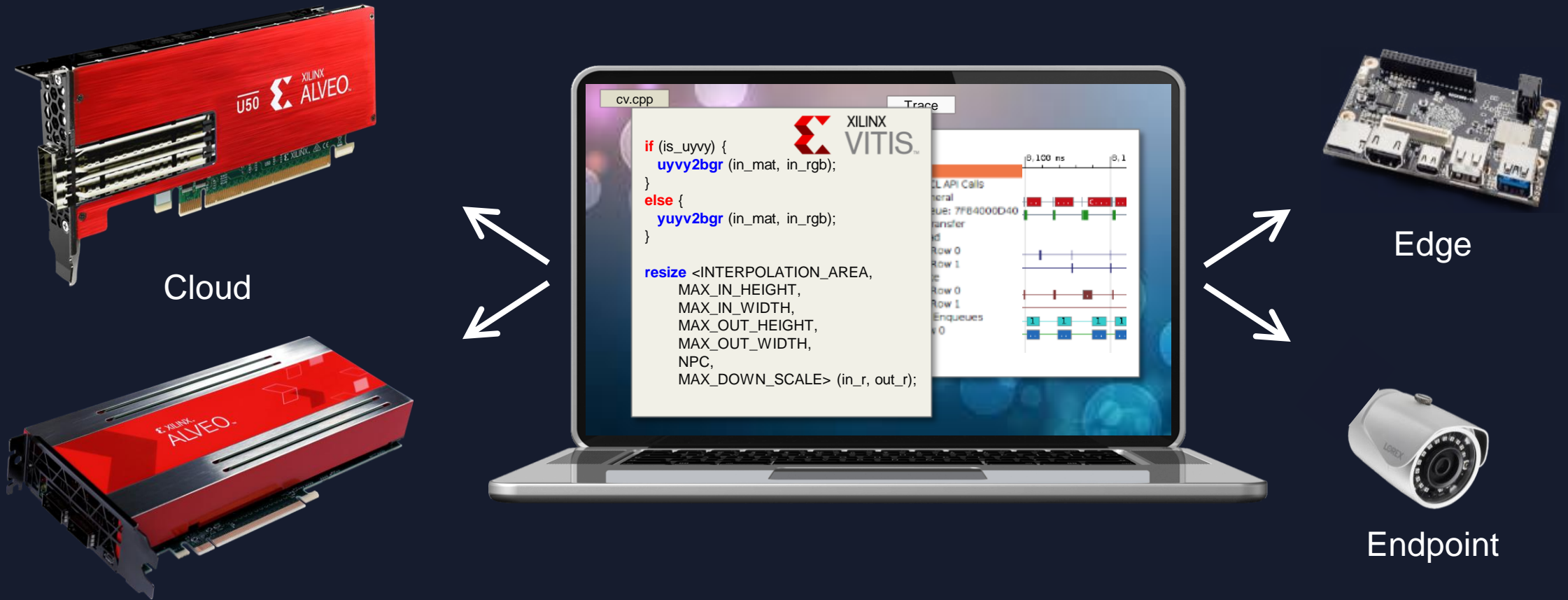
HW / Firmware

VHDL, Verilog

Achieving Pure Software Programmability Today



Achieving Application Portability Today



First Adaptive Computing Challenges

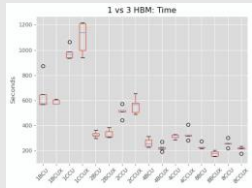
- ▶ Two 6-month events
 - **Developers** (on Hackster.io): ended in Dec
 - **Startups** porting commercial products; ends in Feb
- ▶ Developer challenge results:
 - **1,070** participants
 - **120** free Boards distributed
 - **79** successful submission
 - **9** winners (3 groups x 3 categories)



Developer Contest Winners

Adaptable Compute Acceleration (U50)

Reinforcement Learning with Binarized NN
New Zealand



Intelligent Video Analytics (ZCU104)

Facemask Detector (and Thermal)
Mexico

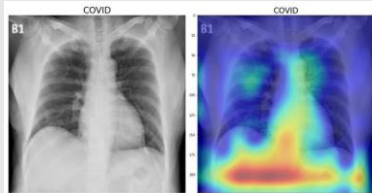


Adaptive IoT (U96)

Hand-Gesture Controlled Drone
VT, USA



AI-based X-ray Covid detection
Greece



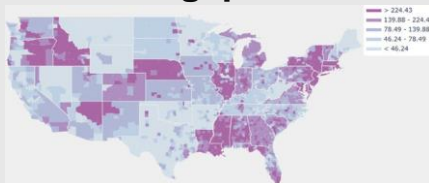
Automatic Fall Detection for Elderly People
India



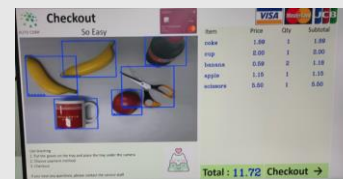
Real-Time 3D Perception
Ireland



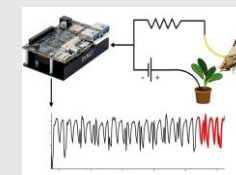
COVID19 Prediction with Graph
Singapore



Real-time Smart PoS System
Taiwan



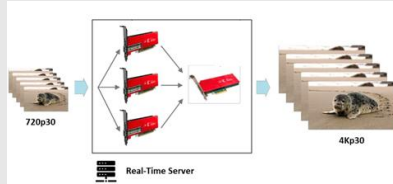
Time Series Similarity Prediction
CA, USA



Start-up Contest Submissions

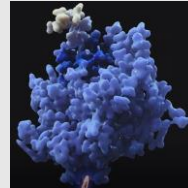
AI-based Super Resolution
Bluedot, Korea

1st



MD simulation for drug discovery
Snowlake, China

2nd



Interactive gaming analytics
Katoid, Spain

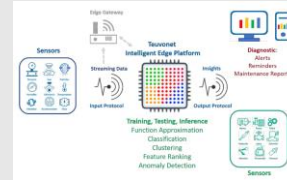
3rd



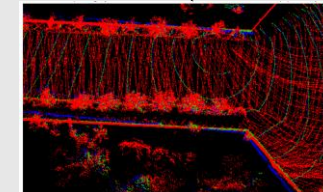
Public Health Monitoring
Datavia System, USA



Simplifying AI on the edge
Teuonet, USA



Point Cloud Registration for Self Driving
Novauto, China



Pentesting Acceleration
Xelera, Germany



Instant Application Acceleration
InAccel, Greece



Privacy-preserving accelerated genomics
CeRICT, Italy



Agenda



Developer Programs

Xilinx Programs for Developers and Partners

DC Accelerator Program



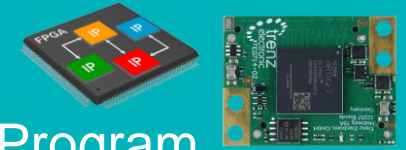
For Alveo or cloud solution providers

Legal Entity



Developer

Alliance Partner Program



For board, IP and design service providers

Legal Entity



Developer

Xilinx Developer Program

For individual developers

Name: Andy Luo
Reputation Score: 750,000 (top 0.01% overall)

DEVELOPER Xilinx Certified Developer

Premier Silver Premier Gold Premier Platinum Premier 1K

Pneumonia and COVID-19 Detection from X-Ray
ThunderGP: Fast Graph Processing for HLS-based
Vitis-AI 1.2 Flow for Avnet VITIS Platforms

XILINX. On-Demand Training 20+ Courses Register Now

XILINX. hacksterio Sign Up for The Adaptive Computing Challenge Register Today

Announcing the Xilinx Developer Program

Why Join

1

Access to
free training
courses
& certification

2

Showcase
your work
on the Xilinx
Developer site

3

Invitations for
developer-
only events &
activities

4

Early access
programs
& release
notifications

Xilinx
Certified
Developer

Free for **all** developers

Developer Program Secure Site

Home / Developer Program Secure Site

Overview Training

Free On-Demand Training Courses for Developers

Accelerating Applications with the Vitis Unified Software Environment

Video Title	Description
Introduction to the Vitis Unified Software Platform	Explains how software/hardware engineers and application developers can be productive in the Vitis unified software environment and OpenCL framework.
Vitis IDE Tool Overview	Describes the elements of the development flow, such as software emulation, as well as debugging support for the host code and kernel code.
Vitis Command Line Flow	Introduces the Vitis environment makefile flow where the user manages the code and dependencies.
Vitis Accelerated Libraries	Describes the Vitis accelerated libraries that are available for domain-specific applications. These libraries are open-source, performance-optimized libraries that offer out-of-the-box acceleration.
Creating a Vitis Embedded Acceleration Platform (Edge)	Describes the Vitis embedded acceleration platform, which provides product development and deployment for creating embedded software and accelerated applications on heterogeneous platforms, including SoCs, and Alveo data center cards.

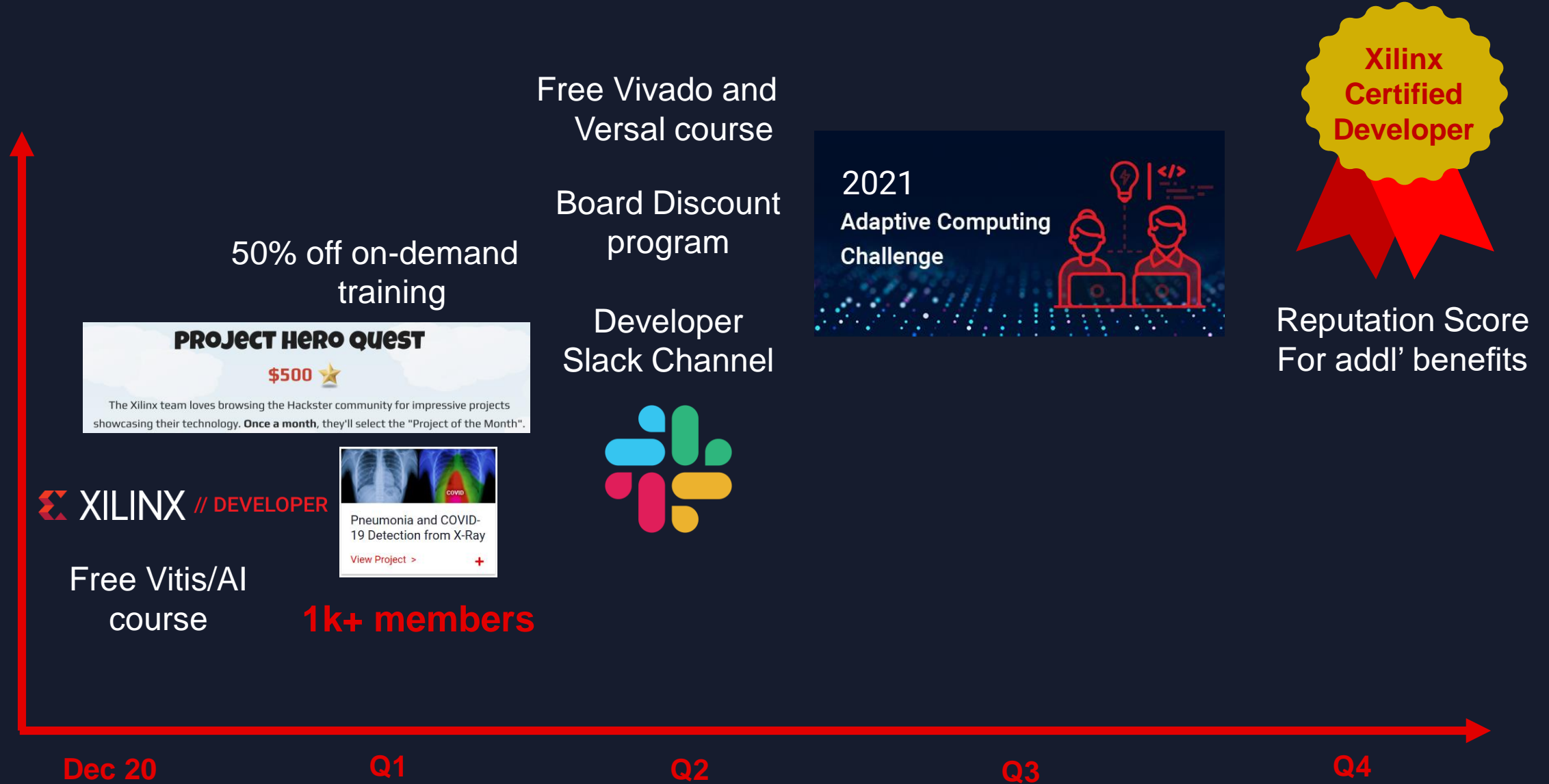
Developing AI Inference Solutions with the Vitis AI Platform

Video Title	Description
Introduction to the Vitis AI Development Environment	Describes the Vitis AI development environment, which consists of the Vitis AI development environment and Xilinx hardware platforms, including both edge devices and Alveo accelerator cards.
Frameworks Supported by the Vitis AI Development Environment	Discusses the support for many common machine learning frameworks such as TensorFlow, PyTorch, and MXNet.
Setting Up the Vitis AI Development Environment	Demonstrates the steps to set up a host machine for developing and running AI applications on Vitis AI development environment embedded devices.

Using Xilinx Alveo Cards to Accelerate Dynamic Workloads

Video Title	Description
Using Xilinx Alveo Cards to Accelerate Dynamic Workloads	An overview of the Alveo™ Data Center accelerator cards with an emphasis on accelerating dynamic workloads using Alveo cards using Nimble Cloud and the Vitis™ unified software platform.

Developer Program Roadmap



Xilinx Adapt Sessions

- ▶ The largest virtual event with 6000+ live attendees
- ▶ Over 20 sessions all available to Developer Program member



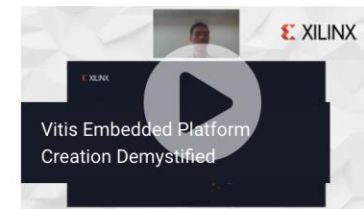
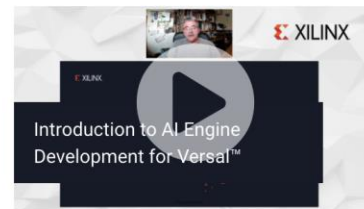
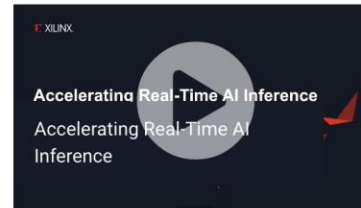
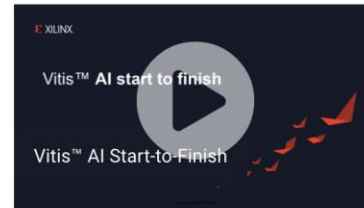
Xilinx Adapt: Software & AI
View On Demand



Xilinx Adapt: Vivado
View On Demand

Xilinx Adapt Technical Session Recordings

Vitis™ and Vitis AI Session Recordings

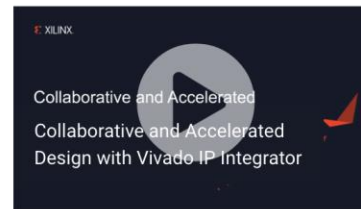


“These features will enable the integrated SW/HW engineering teams of the future.”

“I am excited how the development is moving to SW based; I'm an embedded developer so this is great!”

“This is revolutionary! So many possible future possibilities for collaboration, mentoring.”

Vivado® Session Recordings

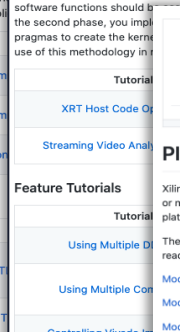
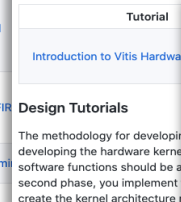
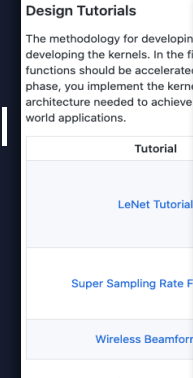


Developer Resources

- **Developer.Xilinx.com:**
 - Developer portal for SW and AI
 - 200+ Articles, projects, videos, trainings, contest
- **Self-guided tutorials on GitHub**
 - 58 tutorials on AI, software acceleration and Versal
 - 12 AI Engine / Versal optimization

Well done. I found the material very useful and the quality rather high. I'm excited about these tutorials and I believe many share the same vision. Maybe I'll get a chance to contribute in the future myself with some additional tutorials :).

A software developer



Agenda



App Store

Evaluation & Monetization

The First Xilinx App Store

Application for Diversified Markets

[See all Apps](#)



The One-Stop Shop to Eval and Buy Xilinx Solutions



EXCLUSIVE

- ▶ FPGA Apps and IP Cores
- ▶ High quality turnkey solutions
- ▶ Curated by Xilinx experts



EASY

- ▶ Self-service online store
- ▶ Instant free trials
- ▶ Online payment & invoicing



SCALABLE

- ▶ Pay-per-Use
- ▶ Subscriptions
- ▶ Per node, hour, or data volume

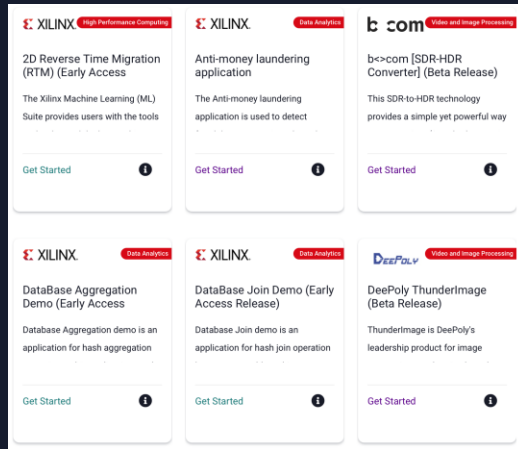


PORTABLE

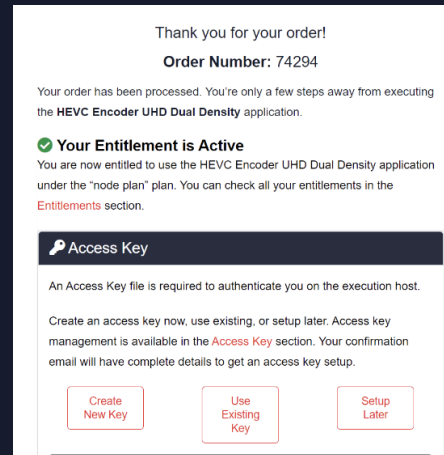
- ▶ Any Xilinx FPGAs anywhere
- ▶ On-prem, cloud, edge, embedded
- ▶ Containerized Apps

Three-Step, Ten-mins to Evaluation on Alveo or Cloud

Step 1: Select an app

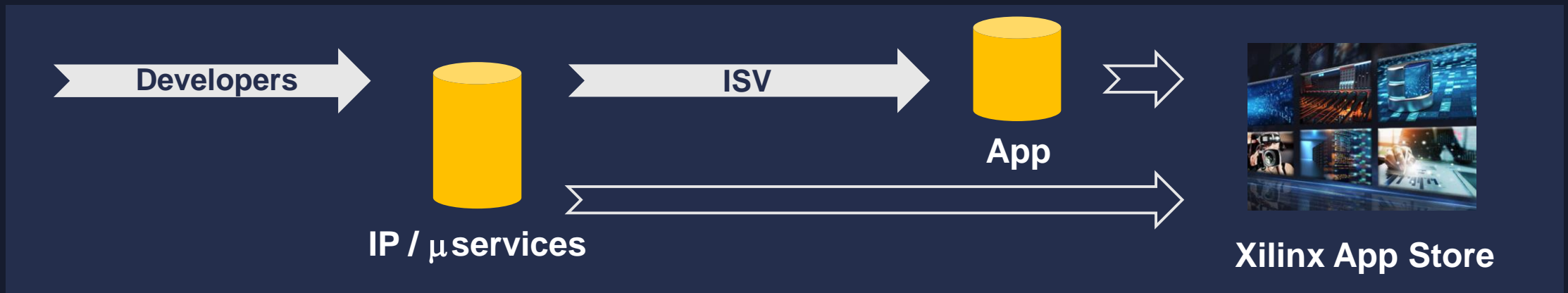
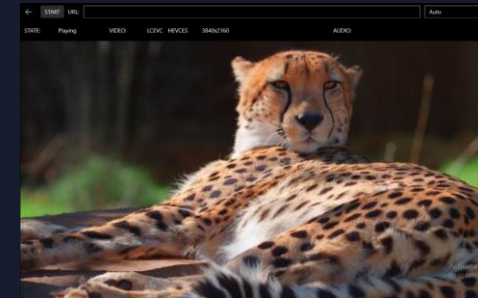


Step 2: Entitlementment



Step 3: Download & Run

```
docker pull hubxilinx/vnova_pplus_alveo_u200:ppxde-demo
docker run -v /tmp/cred.json:/vnova_pplus/cred.json:Z
```



LIVE DEMO


Xilinx App Store

Application for Diversified Markets

[See all Apps](#)



TRANSCODING

 XILINX HEVC/H.265 Bitrate Optimized

 XILINX AVC/H.264 Bitrate Optimized

 XILINX HEVC/AVC High Density

VIDEO / IMAGE PROCESSING

 VNOVA 4K HEVC/LCEVC Video Encoder

 b<>com SDR-HDR Video Converter


 DEEPOLY HEIF Image Encoder

 socionext H264 Video Encoder

DATABASE / ANALYTICS

 MEGH COMPUTING Video Analytics Solution

 Axonerve™ Ultra Fast Search & Replace

 Rapand Big Data Streaming Acceleration

AI/ML

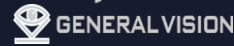
 PEAKSPEED

Geospatial Orthorectification



Myrtle.ai

MAU Accelerator



GENERAL VISION

Posit Tensor Processing Unit
Cascadable Cluster for RTML



STILLWATER
SUPERCOMPUTING, INC.

Visual Search Engine



MOFFETT AI

Mixed Quantization



plumerai

Xtream Facial Recognition



Aupera

Voice Capture for AI Applications



Aaware Inc.
Acoustically Aware™

Posit Processors



VIVIDSPARKS

AI Co-Exploration engine



EDGE CORTIX

NETWORKING



LDA
TECHNOLOGIES™

10G 32-bit ULL MAC



MoSys

Packet Classification



XIPHERA

MACSEC

LIFE SCIENCE

Genetalks

GTX Genomics Suite

STORAGE



Data Compression



bigstream SmartSSD

OTHERS



HDLC Controller



Logic Truit
Technologies

ARINC 818



True RND generator

Wrap Up



- ▶ Vitis enables software and AI developers and adoption going strong
 - Download and start developing today: [Xilinx.com/vitis](https://www.xilinx.com/vitis)



- ▶ Developer Program is here to make you successful developing on Xilinx
 - Sign-up today to enjoy perks: [developer.Xilinx.com](https://developer.xilinx.com)



- ▶ Xilinx App Store enables 10 min to evaluate Xilinx and ISV applications
 - Evaluate today and sign-up to be a seller: [Xilinx.com/appstore](https://www.xilinx.com/appstore)

Xilinx Mission

**Building the Adaptable,
Intelligent World**

