

Overview

The purpose of this notification is to communicate that Xilinx is discontinuing some Development Systems LogiCORE™ Products, a Kintex®-7 Characterization Kit and some Virtex®-6 Kits.

Description

As part of Xilinx regular product pruning activities, the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at Xilinx.com.

Products Affected

- [LogiCORE, Embedded Target RNIC](#)
- [Kintex-7 FPGA KC724 Characterization Kit](#)
- [Virtex-6 FPGA ML605 Evaluation Kit](#)
- [Virtex-6 FPGA Connectivity Kit](#)
- [Virtex-6 FPGA Embedded Kit](#)
- [Virtex-6 FPGA Broadcast Connectivity Kit](#)

Table 1: LogiCORE, Embedded Target RNIC

Part Number	Description	Replacement Part
EF-DI-ETRNIC-SITE	LogiCORE, Embedded Target RNIC, Site License	EF-DI-ERNIC-SITE
EFR-DI-ETRNIC-SITE	LogiCORE, Embedded Target RNIC, Site License Support Renewal	EFR-DI-ERNIC-SITE
EF-DI-ETRNIC-WW	LogiCORE, Embedded Target RNIC, WorldWide License	EF-DI-ERNIC-WW
EFR-DI-ETRNIC-WW	LogiCORE, Embedded Target RNIC, WorldWide License Support Renewal	EFR-DI-ERNIC-WW

Table 2: Kintex-7 FPGA KC724 Characterization Kit

Part Number	Description	Replacement Part
CK-K7-KC724-G	Kintex-7 FPGA KC724 Characterization Kit	EK-K7-KC705-G
CK-K7-KC724-G-J	Kintex-7 FPGA KC724 Characterization Kit, Japan Specific	EK-K7-KC705-G-J

Table 3: Virtex-6 FPGA ML605 Evaluation Kit

Part Number	Description	Replacement Part
EK-V6-ML605-G	Virtex-6 FPGA ML605 Evaluation Kit	EK-K7-KC705-G*
EK-V6-ML605-G-J	Virtex-6 FPGA ML605 Evaluation Kit, Japan Specific	EK-K7-KC705-G-J*

Table 4: Virtex-6 FPGA Connectivity Kit

Part Number	Description	Replacement Part
DK-V6-CONN-G	Virtex-6 FPGA Connectivity Kit	DK-K7-CONN-G*
DK-V6-CONN-G-J	Virtex-6 FPGA Connectivity Kit, Japan Specific	DK-K7-CONN-G-J*

Table 5: Virtex-6 FPGA Embedded Kit

Part Number	Description	Replacement Part
DK-V6-EMBD-G	Virtex-6 FPGA Embedded Kit	DK-K7-EMBD-G*
DK-V6-EMBD-G-J	Virtex-6 FPGA Embedded Kit, Japan Specific	DK-K7-EMBD-G-J*

Table 6: Virtex-6 FPGA Broadcast Connectivity Kit

Part Number	Description	Replacement Part
DK-V6-BCCN-G	Virtex-6 FPGA Broadcast Connectivity Kit	EK-K7-KC705-G*
DK-V6-BCCN-G-J	Virtex-6 FPGA Broadcast Connectivity Kit, Japan Specific	EK-K7-KC705-G-J*

* While Kintex-7 kits are not suitable evaluation platform for the Virtex-6 architecture, they are useful for customers comparing Kintex-7 vs Virtex-6 silicon for new design starts.

Key Dates and Ordering Information

LogiCORE, Embedded Target RNIC

Final orders accepted until April 19, 2019.

Final shipments will be until May 3, 2019.

Virtex-6 FPGA ML605 Evaluation Kit

Virtex-6 FPGA Embedded Kit

Virtex-6 FPGA Broadcast Connectivity Kit

Final orders accepted until April 30, 2019 or until inventory is exhausted whichever comes first.

Final shipments will be until May 30, 2019.

Kintex-7 FPGA KC724 Characterization Kit

Virtex-6 FPGA Connectivity Kit

Final orders accepted until July 31, 2019 or until inventory is exhausted whichever comes first.

Final shipments will be until August 31, 2019.

Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis. All orders placed within 60 days of the last time buy date will be non-cancellable and non-returnable (NCNR). For additional information or questions, contact your local Xilinx Sales representative.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<http://www.xilinx.com/support>). Register today and personalize your “Documentation and Design Advisory Alerts” area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
02/25/2019	1.0	Initial release.
02/25/2019	1.1	Added footnote for Tables 3, 4, 5 and 6.

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